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DATE: Tuesday, December 26, 2006

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L12	L11 and l7 and l2	23
<input type="checkbox"/>	L11	375/\$3.ccls. or 327/\$4.ccls. or 329/\$3.ccls. or 332/\$4.ccls. or 370/\$4.ccls.	274631
<input type="checkbox"/>	L10	l7 and l4 and l2	1
<input type="checkbox"/>	L9	l2 and l4 and l3	0
<input type="checkbox"/>	L8	((multi\$5 or plurla\$4) near3 ((pulse near2 width\$ near2 modulat\$3) or pwm)) and (driver\$) and (full near2 bridge\$) and (inverter\$3) and delay	28
<input type="checkbox"/>	L7	((pulse near2 width\$ near2 modulat\$3) or pwm) and (driver\$) and (full near2 bridge\$) and (inverter\$3) and delay	411
<input type="checkbox"/>	L6	((pulse near2 width\$ near2 modulat\$3) or pwm) and (driver\$) and (full near2 bridge\$)	1537
<input type="checkbox"/>	L5	L4 and L3	0
<input type="checkbox"/>	L4	(375/238).ccls. or (327/31).ccls. or (327/38).ccls. or (329/312).ccls. or (329/311).ccls. or (332/109).ccls. or (332/106).ccls. (370/205).ccls.	2731
<input type="checkbox"/>	L3	L2 and L1	146
<input type="checkbox"/>	L2	@ad<20040421	25761033
<input type="checkbox"/>	L1	((pulse near2 width\$ near2 modulat\$3) or pwm) same (driver\$) same (full near2 bridge\$)	184

END OF SEARCH HISTORY

Day : Tuesday
Date: 12/26/2006

Time: 11:22:31

 **PALM INTRANET**

Inventor Information for 10/828816

Inventor Name	City	State/Country
GABORIAU, JOHANN GUY	AUSTIN	TEXAS
HAGGE, MELVIN L.	ROUND ROCK	TEXAS
ZHANG, LINGLI	AUSTIN	TEXAS
MELANSON, JOHN LAURENCE	AUSTIN	TEXAS

[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity/Reexam](#)[Foreign L](#)Search Another: Application# or Patent# PCT / / or PG PUBS # Attorney Docket # Bar Code #

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Tuesday
Date: 12/26/2006

Time: 11:22:36

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = GABORIAU

First Name = JOHANN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
11011732	6972705	150	12/14/2004	SIGNAL PROCESSING SYSTEM HAVING AN ADC DELTA-SIGMA MODULATOR WITH SINGLE-ENDED INPUT AND FEEDBACK SIGNAL INPUTS	GABORIAU, JOHANN
11165637	Not Issued	71	06/24/2005	Pre-charge apparatus and method for controlling startup transients in a capacitively-coupled switching power stage	GABORIAU, JOHANN
11165899	Not Issued	30	06/24/2005	Method and apparatus for dynamically assigning addresses on a serial bus	GABORIAU, JOHANN
11317546	Not Issued	93	12/23/2005	NON-INTEGGER DECIMATION USING CASCADED INTERGRATOR-COMB FILTER	GABORIAU, JOHANN
11428210	Not Issued	30	06/30/2006	Signal Processing System with Spreading of a Spectrum of Harmonic Frequencies of a Pulse Width Modulator Output Signal	GABORIAU, JOHANN
11534417	Not Issued	30	09/22/2006	Signal processing system with modified delta sigma modulator quantizer output signals to spread harmonic frequencies of pulse width modulator output signals	GABORIAU, JOHANN
60778712	Not Issued	20	03/03/2006	Electromagnetic interference (EMI) reduction for consecutive edge modulation (CEM) with coordinated channels	GABORIAU, JOHANN
60778714	Not Issued	20	03/03/2006	Electromagnetic interference (EMI) reduction for pulse width modulation (PWM) with coordinated channels	GABORIAU, JOHANN

<u>09954776</u>	<u>6556159</u>	150	09/17/2001	VARIABLE ORDER MODULATOR	GABORIAU, JOHANN G.
<u>10078824</u>	<u>6522278</u>	150	02/19/2002	DIGITAL-TO-ANALOG CONVERTER WITH POWER UP/DOWN TRANSIENT SUPPRESSION AND AUTOMATIC RATE SWITCHING	GABORIAU, JOHANN G.
<u>10234893</u>	<u>6765436</u>	150	09/04/2002	POWER SUPPLY BASED AUDIO COMPRESSION FOR DIGITAL AUDIO AMPLIFIER	GABORIAU, JOHANN G.
<u>10234896</u>	Not Issued	61	09/04/2002	Thermal feedback for switch mode amplification	GABORIAU, JOHANN G.
<u>10329853</u>	<u>7055045</u>	150	12/26/2002	AUTOMATIC MODE DETECTION CIRCUITS FOR CONFIGURING A TERMINAL AS AN OUTPUT TERMINAL IN A FIRST MODE AND AS AN INPUT TERMINAL IN A SECOND MODE	GABORIAU, JOHANN G.
<u>10379742</u>	<u>6933871</u>	150	03/05/2003	FEEDBACK STEERING DELTA-SIGMA MODULATORS AND SYSTEMS USING THE SAME	GABORIAU, JOHANN G.
<u>10767906</u>	Not Issued	41	01/29/2004	Circuits and methods for reducing noise and distortion in pulse width modulation systems	GABORIAU, JOHANN G.
<u>10867264</u>	<u>6853242</u>	150	06/14/2004	POWER SUPPLY BASED AUDIO COMPRESSION FOR DIGITAL AUDIO AMPLIFIER	GABORIAU, JOHANN G.
<u>10938002</u>	Not Issued	61	09/10/2004	Single ended switched capacitor circuit	GABORIAU, JOHANN G.
<u>09825080</u>	<u>6538590</u>	150	04/02/2001	TRANSIENT NOISE REDUCTION CIRCUITS, SYSTEMS AND METHODS IN POWER DIGITAL-TO-ANALOG CONVERTERS	GABORIAU, JOHANN GUY
<u>09825091</u>	<u>6522273</u>	150	04/02/2001	CIRCUITS SYSTEMS AND METHODS FOR POWER DIGITAL-TO-ANALOG CONVERTER PROTECTION	GABORIAU, JOHANN GUY
<u>09825445</u>	<u>6462690</u>	150	04/02/2001	CIRCUIT SYSTEMS AND METHODS FOR MULTIRATE	GABORIAU, JOHANN GUY

				DIGITAL-TO-ANALOG AMPLIFIER SYSTEMS	
<u>09917311</u>	<u>6529074</u>	150	07/26/2001	CIRCUITS AND METHODS FOR OUTPUT IMPEDANCE MATCHING IN SWITCHED MODE CIRCUITS	GABORIAU, JOHANN GUY
<u>10828816</u>	Not Issued	30	04/21/2004	Circuits and methods for reducing distortion and noise in pulse width modulation systems utilizing full-bridge drivers	GABORIAU, JOHANN GUY
<u>11060825</u>	Not Issued	41	02/18/2005	Serial audio output driver circuits and methods	GABORIAU, JOHANN GUY
<u>11088955</u>	Not Issued	30	03/24/2005	Circuits and methods for noise management in systems including an asynchronously- operable data port	GABORIAU, JOHANN GUY
<u>09224390</u>	<u>6147522</u>	150	12/31/1998	REFERENCE VOLTAGE CIRCUITRY FOR USE IN SWITCHED-CAPACITOR APPLICATIONS	GABORIAU, JOHANN GUY

Inventor Search Completed: No Records to Display.

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Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Tuesday
Date: 12/26/2006

Time: 11:22:53


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Inventor Name Search Result

Your Search was:

Last Name = HAGGE

First Name = MELVIN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10043558	6690240	150	01/10/2002	LOW-JITTER LOOP FILTER FOR A PHASE-LOCKED LOOP SYSTEM	HAGGE, MELVIN L.
10612200	6828864	150	07/03/2003	LOW-JITTER LOOP FILTER FOR A PHASE-LOCKED LOOP SYSTEM	HAGGE, MELVIN L.
10767906	Not Issued	41	01/29/2004	Circuits and methods for reducing noise and distortion in pulse width modulation systems	HAGGE, MELVIN L.
10828816	Not Issued	30	04/21/2004	Circuits and methods for reducing distortion and noise in pulse width modulation systems utilizing full-bridge drivers	HAGGE, MELVIN L.
11297016	Not Issued	95	12/08/2005	CENTERED-PULSE CONSECUTIVE EDGE MODULATION (CEM) METHOD AND APPARATUS	HAGGE, MELVIN L.
06444459	4501978	150	11/24/1982	LEVEL SHIFT INTERFACE CIRCUIT	HAGGE, MELVIN L.

Inventor Search Completed: No Records to Display.

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To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Tuesday
Date: 12/26/2006


PALM INTRANET

Time: 11:23:12

Inventor Name Search Result

Your Search was:

Last Name = ZHANG

First Name = LINGLI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10767906	Not Issued	41	01/29/2004	Circuits and methods for reducing noise and distortion in pulse width modulation systems	ZHANG, LINGLI
10828816	Not Issued	30	04/21/2004	Circuits and methods for reducing distortion and noise in pulse width modulation systems utilizing full-bridge drivers	ZHANG, LINGLI
11088955	Not Issued	30	03/24/2005	Circuits and methods for noise management in systems including an asynchronously-operable data port	ZHANG, LINGLI
11165637	Not Issued	71	06/24/2005	Pre-charge apparatus and method for controlling startup transients in a capacitively-coupled switching power stage	ZHANG, LINGLI
11601541	Not Issued	19	11/17/2006	Software transaction commit order and conflict management	ZHANG, LINGLI
09074663	6098018	150	05/08/1998	METHOD AND APPARATUS FOR AN AUTONOMOUS CLOUD RADAR	ZHANG, LINGLING
11178725	Not Issued	30	07/11/2005	Hybrid electric vehicle	ZHANG, LINGLING
60587073	Not Issued	159	07/10/2004	Hybrid electric vehicle	ZHANG, LINGLING

Inventor Search Completed: No Records to Display.

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LINGLI

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Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

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Date: 12/26/2006

Time: 11:23:12


PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = ZHANG

First Name = LINGLI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10767906	Not Issued	41	01/29/2004	Circuits and methods for reducing noise and distortion in pulse width modulation systems	ZHANG, LINGLI
10828816	Not Issued	30	04/21/2004	Circuits and methods for reducing distortion and noise in pulse width modulation systems utilizing full-bridge drivers	ZHANG, LINGLI
11088955	Not Issued	30	03/24/2005	Circuits and methods for noise management in systems including an asynchronously-operable data port	ZHANG, LINGLI
11165637	Not Issued	71	06/24/2005	Pre-charge apparatus and method for controlling startup transients in a capacitively-coupled switching power stage	ZHANG, LINGLI
11601541	Not Issued	19	11/17/2006	Software transaction commit order and conflict management	ZHANG, LINGLI
09074663	6098018	150	05/08/1998	METHOD AND APPARATUS FOR AN AUTONOMOUS CLOUD RADAR	ZHANG, LINGLING
11178725	Not Issued	30	07/11/2005	Hybrid electric vehicle	ZHANG, LINGLING
60587073	Not Issued	159	07/10/2004	Hybrid electric vehicle	ZHANG, LINGLING

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To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Tuesday
Date: 12/26/2006

Time: 11:23:29


PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = MELANSON

First Name = JOHN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09953767	6501692	150	09/17/2001	CIRCUIT AND METHOD FOR STRESS TESTING A STATIC RANDOM ACCESS MEMORY (SRAM) DEVICE	MELANSON, JOHN
10144295	Not Issued	61	05/13/2002	Method and system for providing a codec clock signal at a desired operational rate	MELANSON, JOHN
10341640	6741197	150	01/13/2003	DIGITAL-TO-ANALOG CONVERTER (DAC) OUTPUT STAGE	MELANSON, JOHN
10342625	6839447	150	01/14/2003	SYNCHRONISED BINAURAL HEARING SYSTEM	MELANSON, JOHN
10447606	7162029	150	05/29/2003	GAIN OR INPUT VOLUME CONTROLLER AND METHOD UTILIZING A MODIFIED R2R LADDER NETWORK	MELANSON, JOHN
10685128	7154562	150	10/14/2003	METHODS AND CIRCUITS FOR IMPLEMENTING PROGRAMMABLE GAMMA CORRECTION	MELANSON, JOHN
11012418	Not Issued	30	12/15/2004	Digital implementation of a fourth order linkwitz-riley network with a low cutoff frequency	MELANSON, JOHN
11076790	7015853	150	03/09/2005	DATA CONVERTER WITH REDUCED DIFFERENTIAL NONLINEARITY	MELANSON, JOHN
11082346	Not Issued	30	03/17/2005	Method and system for synchronizing video information derived from an asynchronously sampled video signal	MELANSON, JOHN
11082347	Not Issued	25	03/17/2005	Method and system for video-synchronous audio clock generation from an	MELANSON, JOHN

				asynchronously sampled video signal	
<u>11088446</u>	<u>7158045</u>	150	03/24/2005	METHOD AND APPARATUS FOR MAINTAINING AN IDEAL FREQUENCY RATIO BETWEEN NUMERICALLY-CONTROLLED FREQUENCY SOURCES	MELANSON, JOHN
<u>11232650</u>	Not Issued	30	09/22/2005	Method and apparatus for canceling jitter in a fractional-N phase-lock loop (PLL)	MELANSON, JOHN
<u>11232808</u>	Not Issued	30	09/22/2005	Digital filter having improved overload recovery characteristics	MELANSON, JOHN
<u>11345693</u>	Not Issued	25	02/01/2006	Synchronized binaural hearing system	MELANSON, JOHN
<u>60588951</u>	Not Issued	159	07/19/2004	Signal processing systems with look-ahead delta sigma modulators	MELANSON, JOHN
<u>60612474</u>	Not Issued	159	09/23/2004	Intelligent room calibration technology for room equalization and speaker set-up	MELANSON, JOHN
<u>60729685</u>	Not Issued	159	10/24/2005	Techniques for PWM current DAC with low bandwidth PLL	MELANSON, JOHN
<u>60778711</u>	Not Issued	20	03/03/2006	Stereo output amplifier with small capacitor	MELANSON, JOHN
<u>60778712</u>	Not Issued	20	03/03/2006	Electromagnetic interference (EMI) reduction for consecutive edge modulation (CEM) with coordinated channels	MELANSON, JOHN
<u>60778714</u>	Not Issued	20	03/03/2006	Electromagnetic interference (EMI) reduction for pulse width modulation (PWM) with coordinated channels	MELANSON, JOHN
<u>60822193</u>	Not Issued	20	08/11/2006	Battery Protection Circuit	MELANSON, JOHN
<u>60825221</u>	Not Issued	20	09/11/2006	Low-Jitter Hybrid Phase Locked-Loop	MELANSON, JOHN
<u>60826664</u>	Not Issued	20	09/22/2006	Hybrid Analog Digital Phase Locked Loop Using Direct Digital Synthesis Numerically Controlled Oscillator	MELANSON, JOHN
<u>60826757</u>	Not Issued	20	09/25/2006	Hybrid Analog Digital Phase Locked Loop Using Direct Digital Synthesis Numerically Controlled Oscillator	MELANSON, JOHN

<u>09165825</u>	<u>6434246</u>	150	10/02/1998	APPARATUS AND METHODS FOR COMBINING AUDIO COMPRESSION AND FEEDBACK CANCELLATION IN A HEARING AID	MELANSON, JOHN LAURENCE
<u>07123090</u>	<u>4816261</u>	150	11/20/1987	DEODORANT GEL STICK	MELANSON, JOHN D.
<u>07314627</u>	<u>4906454</u>	150	02/23/1989	DEODORANT COMPOSITIONS CONTAINING SPECIFIC PIROCTONE SALTS AND PERFUMES	MELANSON, JOHN D.
<u>11383125</u>	Not Issued	25	05/12/2006	METHOD AND SYSTEM FOR SURROUND SOUND BEAM-FORMING USING THE OVERLAPPING PORTION OF DRIVER FREQUENCY RANGES	MELANSON, JOHN L
<u>11531159</u>	Not Issued	30	09/12/2006	FEEDBACK TOPOLOGY DELTA-SIGMA MODULATOR HAVING AN AC-COUPLED FEEDBACK PATH	MELANSON, JOHN L
<u>11609894</u>	Not Issued	19	01/01/0001	CHARGE-PUMP BIASED BATTERY PROTECTION CIRCUIT	MELANSON, JOHN L
<u>11611069</u>	Not Issued	19	01/01/0001	METHOD AND APPARATUS FOR CONTROLLING A SELECTABLE VOLTAGE AUDIO POWER OUTPUT STAGE	MELANSON, JOHN L
<u>11614368</u>	Not Issued	19	01/01/0001	HYBRID ANALOG/DIGITAL PHASE-LOCK LOOP FOR LOW-JITTER SYNCHRONIZATION	MELANSON, JOHN L
<u>09241516</u>	<u>6173063</u>	150	10/06/1998	OUTPUT REGULATOR FOR FEEDBACK REDUCTION IN HEARING AIDS	MELANSON, JOHN L
<u>10219362</u>	<u>6738004</u>	150	08/15/2002	METHOD AND SYSTEM OF INTEGRATING A MISMATCH NOISE SHAPER INTO THE MAIN LOOP OF A DELTA-SIGMA MODULATOR	MELANSON, JOHN L.
<u>10234893</u>	<u>6765436</u>	150	09/04/2002	POWER SUPPLY BASED AUDIO COMPRESSION FOR DIGITAL AUDIO AMPLIFIER	MELANSON, JOHN L.
<u>10234896</u>	Not	61	09/04/2002	Thermal feedback for switch	MELANSON, JOHN

	Issued			mode amplification	L.
<u>10314804</u>	<u>6762704</u>	150	12/09/2002	MODULATION OF A DIGITAL INPUT SIGNAL USING MULTIPLE DIGITAL SIGNAL MODULATORS	MELANSON, JOHN L.
<u>10325145</u>	<u>6693571</u>	150	12/20/2002	MODULATION OF A DIGITAL INPUT SIGNAL USING A DIGITAL SIGNAL MODULATOR AND SIGNAL SPLITTING	MELANSON, JOHN L.
<u>10425285</u>	<u>6842486</u>	150	04/29/2003	SIGNAL PROCESSING SYSTEM WITH BASEBAND NOISE MODULATION AND NOISE FOLD BACK REDUCTION	MELANSON, JOHN L.
<u>10425448</u>	<u>6961385</u>	150	04/29/2003	SIGNAL PROCESSING SYSTEM WITH BASEBAND NOISE MODULATION CHOPPER CIRCUIT TIMING TO REDUCE NOISE	MELANSON, JOHN L.
<u>10665164</u>	<u>6998923</u>	150	09/18/2003	LOW-NOISE LOOP FILTER FOR A PHASE-LOCKED LOOP SYSTEM	MELANSON, JOHN L.
<u>10668397</u>	<u>6861968</u>	150	09/23/2003	SIGNAL PROCESSING SYSTEM WITH BASEBAND NOISE MODULATION AND NOISE FILTERING	MELANSON, JOHN L.
<u>10682552</u>	Not Issued	94	10/09/2003	DELTA SIGMA MODULATOR WITH INTEGRAL DECIMATION	MELANSON, JOHN L.
<u>10682566</u>	<u>6822594</u>	150	10/09/2003	OVERLOAD PROTECTION AND STABILITY FOR HIGH ORDER 1-BIT DELTA-SIGMA MODULATORS	MELANSON, JOHN L.
<u>10684997</u>	<u>6844840</u>	150	10/14/2003	SUCCESSIVE-APPROXIMATION-REGISTER (SAR) ANALOG-TO-DIGITAL CONVERTER (ADC) AND METHOD UTILIZING N THREE-WAY ELEMENTS	MELANSON, JOHN L.
<u>10687416</u>	<u>7091771</u>	150	10/16/2003	SEGMENTED CHOPPING AMPLIFIER	MELANSON, JOHN L.
<u>10770186</u>	<u>7049852</u>	150	02/02/2004	FRACTIONAL-INTEGER PHASE-LOCKED LOOP SYSTEM WITH A FRACTIONAL-FREQUENCY-	MELANSON, JOHN L.

				INTERVAL PHASE FREQUENCY DETECTOR	
10856436	Not Issued	25	05/28/2004	Audio clocking in video applications	MELANSON, JOHN L.
10867264	6853242	150	06/14/2004	POWER SUPPLY BASED AUDIO COMPRESSION FOR DIGITAL AUDIO AMPLIFIER	MELANSON, JOHN L.
10875920	6879275	150	06/24/2004	SIGNAL PROCESSING WITH A LOOK-AHEAD MODULATOR HAVING TIME WEIGHTED ERROR VALUES	MELANSON, JOHN L.

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Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)